



op 2188
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: William P. Ward
Serial No.: 09/651,229
Filed: August 25, 2000
For: Providing Multiple Memory
Controllers on a Memory Bus

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Group Art Unit: N/A
Examiner: N/A
Atty. Dkt. No.: NCRC-0020-US
(9295)

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Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Please renumber claim 27 to claim ²33.

The Patent Office is authorized to charge any additional fees and/or credit any overpayments to Deposit Account 14-0225 (9295).

Respectfully submitted,

Date:

12/1/00

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I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on December 1, 2000 and is addressed to the Commissioner for Patents, Washington, DC 20231.

Corey McGowan